Registration Form

1. **Name:**
2. **Dept./Institute:**
3. **Batch/Year:**
4. **Degree Pursuing:**
5. **Address for Correspondence:**

Mobile:

E-mail:

Declaration

The information furnished above is true to the best of my knowledge.

Date:
Place:

Signature of Applicant

Approval from HOD

Important Dates

Last date for the receipt of applications:
13th February 2019
Date of intimation regarding selection:
14th February 2019

Registration Fee

Registration Fee:-
250/- per student (To be paid in Cash with the filled form or NEFT Account No. 09001000018949
Name: Registrar, Indira Gandhi Delhi Technical University for Women
Bank: - Punjab and Sind Bank Branch: Kashmere Gate, Delhi-110006
IFSC Code: PSIB0001098)

No Lunch, only Refreshments will be given to participants.

Since there are limited number of seats, registration will be done on **first come first serve basis**.

Send the duly filled hard copy of registration form to HoD ECE Office, Dept. of Electronics & Communication Engineering, Indira Gandhi Delhi Technical University for Women, Kashmere Gate, Delhi-110006 latest by 13th February, 2019. OR A scanned copy of the registration form can be sent at fdp.ece@gmail.com.

A

One week Short Term course for Hands on Training on various Electronic Design Automation Tools

(18th Feb 2019 – 22nd Feb, 2019**)**

Patron

Prof. Amita Dev, Vice –Chancellor, IGDTUW

Advisor

Prof. R. K Singh, Registrar IGDTUW
Dr. Ranu Gadi, Dy. Dean IRD
Dr. Jasdeep Kaur Dhanoa, Assoc. Prof.

Course Coordinator

Dr. Nidhi Goel

Organizing Committee

Dr. Akash Tayal
Ms. Neha Kapoor

Organized by

Dept. of E & C Engg.
IGDTUW- Delhi

Funded by

सत्यमेव जयते
Department of Science and Technology
Ministry of Science and Technology
Government of India

CURIE
Department of Science & Technology
Government of India

About the College

Indira Gandhi Delhi Technical University for Women (IGDTUW) is a non-affiliating teaching and research University at Delhi to facilitate and promote studies, research, technology, innovation, incubation and extension work in emerging areas of professional education among women, with focus on engineering, technology, applied sciences, management and its allied areas with the objective to achieve excellence in these and related fields.

The objective of the University is to foster industry relevant research and innovations and empower the women of our country through value based higher education making them employable, self reliant, responsible citizen of the country with concern for environment and society.

About the Department

The Department of Electronics and Communication Engineering, IGDTUW has been known for its exceptionally strong Under-Graduate and Post Graduate programmes. It has been dedicated to provide dynamic and quality women engineers to the industry and society. The Department offers one Undergraduate (UG) and two Postgraduate (PG) programmes, M. Tech in VLSI Design course. The Department has always been on a high growth path and has an experienced and dedicated faculty with a strong commitment to engineering education. The Department, offers strong research orientation to students in the areas of Communication Systems, Signal and Image Processing, Embedded systems, Microelectronics and VLSI Design.

About the Short Term Course

Since a modern semiconductor chip can have billions of components, EDA tools are essential for their design. The short term course will cover hands on training on a category of software tools for designing electronic systems such as integrated circuits and printed circuit boards. The tools work together in a design flow that chip designers use to design and analyze entire semiconductor chips. Using these tools, simulation and verification of analog and mixed-signal circuits can be performed.

These tools can also be used to design intellectual properties (IP) covering a broad range of areas, including interfaces, memory, analog, SoC peripherals, dataplane processing units, and verification. Linking of these tools with MATLAB/Simulink, Mentor Graphics and Xilinx will be covered.

This course will be beneficial for B. Tech, M. Tech and Research scholars who are interested to learn and cultivate new thoughts in the field of Electronic Design and Automation Tools. This will provide an opportunity to enrich their knowledge and enhance their technical growth by interaction from various specialists from industry.

Tentative Schedule

Day	Topic	Speaker
Day 1	Mentor Graphics	Industry Expert
Day 2	Mentor Graphics	
Day 3	Xilinx Vivado	Industry Expert
Day 4	MATLAB	Industry Expert
Day 5	MATLAB	

Venue

DSP Lab (Electrical Block)
Department of Electronics Engineering,
IGDTUW
Kashmere Gate, Delhi-110006